

Ram Vijayagopal

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Professional Experience

- **March 2008-Present.** Assistant Systems Analysis Engineer in the Center for Transportation Research, Energy Systems Division at Argonne National Laboratory. Member of the Powertrain Systems Analysis Toolkit development team
- **December 2003-August 2006.** Engineer at Hitachi Automotive Systems in Ibaraki-ken, Japan. Worked in the Control Group of the Electric Powertrain Development Center on developing, implementing and testing the motor control for a mild hybrid system. Key responsibilities were
 - Design and development of control algorithms for a motor/generator unit
 - Micro-controller programming
 - Hardware in the loop simulation
 - Implementing controller area network communication modules in the micro-controller
 - Modeling and simulation of hybrid sub-systems in Matlab/Simulink
 - Developing on-board diagnostics, fault recognition and fault tolerant strategies
 - Testing and validation of the sub-systems and interaction with clients
- **June 2000-November 2003.** Engineer at Mahindra Consulting Ltd in Bombay, India. Part of the team which developed and implemented the product lifecycle management system for one of the largest automobile manufacturers in India, including
 - Change management and document management
 - Configurable product development
 - Automatic update and conversion of engineering bill of materials (BOM) to process BOM

Education

- MS, Mechanical Engineering, University of Michigan-Ann Arbor, 2008
- BT, Mechanical Engineering, University of Kerala; Trivandrum, Kerala, India, 2000

Awards

- Hindu-Hitachi scholarship, instituted by Hitachi Corporation, Japan and The Hindu, one of India's foremost publishing houses (2002)
- State Committee for Science and Technology Research Grant (1998-2000)

Professional Society Affiliations

- Member, Society of Automotive Engineers

Publications & Patents

- 1 peer-reviewed conference paper
- 1 presentation